Notice of Allowability	Application No.	Applicant(s)	
	10/750,057	LAKE ET AL.	
	Examiner	Art Unit	
	Zhuo H. Li	2185	
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in or other appropriate community (IGHTS). This application is s	n this application. If not included unication will be mailed in due c	d ourse. THIS
1. \square This communication is responsive to <u>8/24/07</u> .			
2. 🔀 The allowed claim(s) is/are <u>1-32</u> .			
3. Acknowledgment is made of a claim for foreign priority u a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN	e been received. e been received in Application cuments have been received. of this communication to file	on No d in this national stage applicati	
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 4. A SUBSTITUTE OATH OR DECLARATION must be submin INFORMAL PATENT APPLICATION (PTO-152) which give			OTICE OF
5. CORRECTED DRAWINGS (as "replacement sheets") mu	est be submitted.	•	
(a) ☐ including changes required by the Notice of Draftsper		w (PTO-948) attached	
1) hereto or 2) to Paper No./Mail Date	_•	,	
(b) including changes required by the attached Examiner Paper No./Mail Date	's Amendment / Comment or	r in the Office action of	
Identifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in	1.84(c)) should be written on t the header according to 37 CF	he drawings in the front (not the l FR 1.121(d).	pack) of
 DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT 			ote the
Attachment(s) 1. ⊠ Notice of References Cited (PTO-892)	5. ☐ Notice of In	formal Patent Application	
2. Notice of Draftperson's Patent Drawing Review (PTO-948)		6. Interview Summary (PTO-413), Paper No./Mail Date	
 Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 		Amendment/Comment	
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. 🛛 Examiner's	Statement of Reasons for Allov	vance
	9. 🗌 Other	9. Other	

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EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE

1. Claims 1-32 are allowed over prior arts.

2. The following is an examiner's statement of reasons for allowance:

Applicant's invention is drawn to a method for scanning a configuration memory space located in an identification register whose value matches a predetermined value, i.e., known offset, in order to allow hardware designers to move pre-defined structures to different locations within the configuration space of a device.

Applicant's independent method claims 1 and 17, each recites, *inter alia*, scanning an address space to locate a structure, as defined in the specification (pages 5-8), wherein a identification register identifies a starting address location of the structure within the address space, reading a second capabilities pointer if the first identification value of the first identification register dose not matches the predetermined value, determining the starting address location of the structure from the identification register whose value matches the predetermined value, and accessing a register located within the structure by adding a predetermined offset to the starting address location of the structure. These steps, in combination of the remaining steps, are neither taught nor suggested by the prior art.

Accordingly, Applicant's claims are allowed for these reasons and for the reasons recited in the previous Amendments and Remarks.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue

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fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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- 3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Betker et al. (US PAT. 5,909,557) discloses a bus interface for configuring a processor (100), allows the processor to interface with external buses of difference types, wherein the buses have different data widths, i.e., 8-bits bus transfer, or 16-bits transfer, (figure 1, and col. 3 line 19 to col. 4 line 63), which Betker neither teaches the steps of, reading a second capabilities pointer if the first identification value of the first identification register dose not matches the predetermined value, determining the starting address location of the structure from the identification register whose value matches the predetermined value, and accessing a register located within the structure by adding a predetermined offset to the starting address location of the structure.
- 4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Zhuo H. Li whose telephone number is 571-272-4183. The examiner can normally be reached on Mon - Fri 10:00am - 6:30pm..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sanjiv Shah can be reached on 571-272-4098. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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5. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Zhuo H. Li

Patent Examiner November 6, 2007

SANJIV SHAH
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100